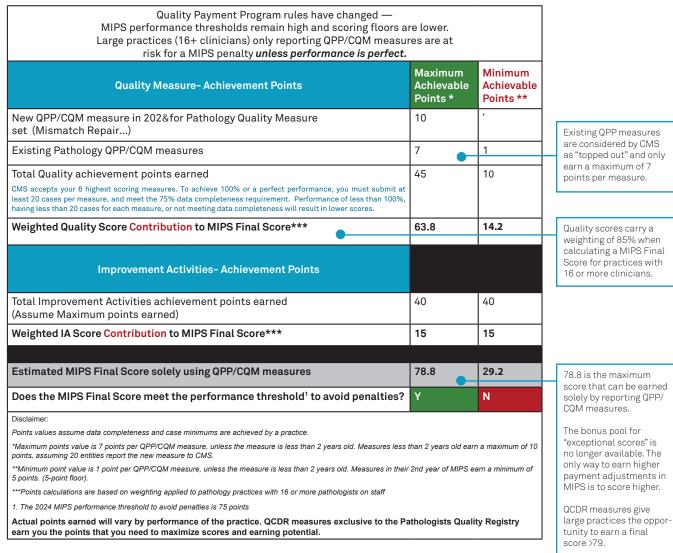


It's practically impossible to meet the MIPS 202&Performance Threshold without the use of QCDR measures



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